Errata

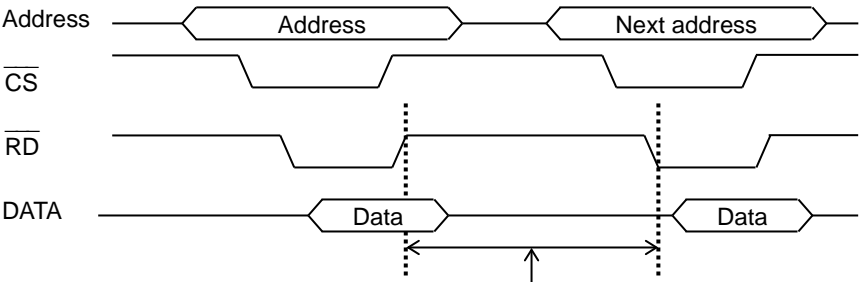**"G9001A/G9002A User's Manual" (DA70131-1/3E)** contains the error described below.

Please confirm the following corrections.

Page	Corrected part	Incorrect	Correct
III-9 III-10	1-2-1-3. Read timing	The following figure and description is missing and needed as 3 after 2. 3.Uses the WRQ output (CPU has a wait function) and status reading or FIFO reading. Although you use WRQ output, it is necessary to secure a time of 100 ns or more depending on the software. Note 1: Although you continue to read processing, if there is an interval of 100 ns or more due to the processing of the CPU, you do not need to insert software-wait. Note 2: For access other than status reading or FIFO reading, you do not need to insert software-wait if WRQ output is used. Note 3: If you do not use the WRQ output, use it at the timing of 1).	
III-8	1-2-1. Normal access The center device has 9 address terminals used to access 512 bytes of memory. The access timing for each of these addresses is shown below. CPUs that have a wait function can be connected to the WRQ terminal on the center device so that they can be used without special concern for signal timing. However, CPUs without a wait function must monitor the IFB output or use one of the following timing schemes (this is essential).	The underlined descriptions on the right are needed.	The center device has 9 address terminals used to access 512 bytes of memory. The access timing for each of these addresses is shown below. CPUs that have a wait function can be connected to the WRQ terminal on the center device so that they can be used without special concern for signal timing other than status reading and FIFO reading. However, when using status reading and FIFO reading or CPUs without a wait function, you must monitor the IFB output or use one of the following timing schemes (this is essential)

Page	Corrected part	Incorrect	Correct
Ⅲ-10	1-2-1-3. Read timing 2) Uses the WRQ output (CPU has a wait function)	The underlined description on the right is needed.	2) Uses the WRQ output (CPU has a wait function) and does not use status reading nor FIFO reading

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