

Errata

“Pulse Control LSI PCL6046 User’s Manual” (DA70122-1/2E) contains the error described below.

Please confirm the following corrections.

Page	Corrected part	Incorrect	Correct																								
4	1-2. Features ◆ Servomotor I/F The ERC signal is a pulsed output. The pulse length can be set.	(12 μs to 104 ms. A level output is also available.)	(11 μs to 100 ms. A level output is also available.)																								
7	4. Function of Terminals <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 33%;">Signal name</th> <th style="width: 33%;">Terminal No.</th> <th style="width: 33%;">Input/output</th> </tr> </thead> <tbody> <tr> <td>IF0</td> <td>C1</td> <td></td> </tr> <tr> <td>IF1</td> <td>D3</td> <td></td> </tr> </tbody> </table>	Signal name	Terminal No.	Input/output	IF0	C1		IF1	D3		<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 100%;">Input/output</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">Input</td> </tr> </tbody> </table>	Input/output	Input	<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 100%;">Input/output</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">Input U</td> </tr> </tbody> </table> <p>Supplement) CPU interface setting terminal pins, IF0 and IF1, are pulled up internally. When driving with open collectors, pull up using 5 to 10 kΩ resistors externally</p>	Input/output	Input U											
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43	8-3-8. PRMD (RMD) register <table style="width: 100%; text-align: center;"> <tr> <td style="width: 25%;">31</td> <td style="width: 25%;">30</td> <td style="width: 25%;">29</td> <td style="width: 25%;">28</td> </tr> <tr> <td style="border: 1px solid black;">0</td> <td style="border: 1px solid black;">0</td> <td style="border: 1px solid black;">MSDC</td> <td style="border: 1px solid black;">MIPM</td> </tr> </table>	31	30	29	28	0	0	MSDC	MIPM	<table style="width: 100%; text-align: center;"> <tr> <td style="width: 25%;">31</td> <td style="width: 25%;">30</td> <td style="width: 25%;">29</td> <td style="width: 25%;">28</td> </tr> <tr> <td style="border: 1px solid black;">0</td> <td style="border: 1px solid black;">0</td> <td style="border: 1px solid black;">MSDC</td> <td style="border: 1px solid black;">MIPM</td> </tr> </table>	31	30	29	28	0	0	MSDC	MIPM	<table style="width: 100%; text-align: center;"> <tr> <td style="width: 25%;">31</td> <td style="width: 25%;">30</td> <td style="width: 25%;">29</td> <td style="width: 25%;">28</td> </tr> <tr> <td style="border: 1px solid black;">0</td> <td style="border: 1px solid black;">0</td> <td style="border: 1px solid black;">0</td> <td style="border: 1px solid black;">MIPM</td> </tr> </table> <p>Supplement) Ramping-down point auto setting does not operate correctly when RMD.MSDC=1 is set. Therefore, set always the bit 29 of RMD register to “0”.</p>	31	30	29	28	0	0	0	MIPM
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47	<p>8-3-13. RENV1 register</p> <table border="1"> <tr> <td>14 to 12</td> <td>EPW2 to 0</td> <td>Specify the pulse width of the ERC output signal.</td> </tr> </table>	14 to 12	EPW2 to 0	Specify the pulse width of the ERC output signal.	<p>000 : 12 μs 001 : 102 μs 010 : 409 μs 011 : 1.6 ms 100 : 13 ms 101 : 52 ms 110 : 104 ms 111 : Level output</p>	<p>000 : 11 to 13 μs 001 : 91 to 98 μs 010 : 364 to 391 μs 011 : 1.4 to 1.6 ms 100 : 11 to 13 ms 101 : 46 to 50 ms 110 : 93 to 100 ms 111 : Level output</p>												
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